## Instruction

- Separate into groups of no more than three people.
- Each group uses a sheet of A4 paper.
- Write the name and ID of all group members on the top of the sheet.
- You have 10 minutes to do this quiz.
- Show your work! Write down what you have done to obtain your answer.


## Questions

I. Write down the canonical sums for $\Sigma$ and $c_{\text {out }}$ defined in the given truth table.
2. Find the corresponding K-maps of your answers in Q1.

| Inputs |  | Outputs |  |  |
| :---: | :---: | :---: | :---: | :---: |
| $A$ | $B$ | $C_{n}$ | $C_{\text {ott }}$ | $\Sigma$ |
| 0 | 0 | 0 | 0 | 0 |
| 0 | 0 | 1 | 0 | 1 |
| 0 | 1 | 0 | 0 | 1 |
| 0 | 1 | 1 | 1 | 0 |
| 1 | 0 | 0 | 0 | 1 |
| 1 | 0 | 1 | 1 | 0 |
| 1 | 1 | 0 | 1 | 0 |
| 1 | 1 | 1 | 1 | 1 |

3. Find the minimal sums using the K-maps from Q2.
4. Implement what you got from Q3 with inverters and AND-OR logic.
5. Construct equivalent circuits using NAND gates.

2:29 PM
(1) For each of the outputs in the truth table, Find the rows where the outputs are 1, then add all the minterms corresponding to those rows.

$$
\begin{aligned}
C_{\text {out }} & =\bar{A} B C_{\text {in }}+A \bar{B} C_{i n}+A B \bar{C}_{\text {in }}+A B C_{\text {in }} \\
\Sigma & =\bar{A} \bar{B} C_{\text {in }}+\bar{A} B \bar{C}_{\text {in }}+A \bar{B} \bar{C}_{\text {in }}+A B C_{\text {in }}
\end{aligned}
$$

(2) Put is in the $K$-mays where each 1 corresponds to the minters in (1).


K-map for Coot

$k-\operatorname{map}$ for $\Sigma$
(3) When the 1 is are mapped in the $k$-maps, we then find all prime impliconts.

It turns out that we need every prime implicants (for both cases).
Minimal sums are found by summing the product terms corresponding to the "surviving" prime impliconts.

$$
\begin{aligned}
& C_{\text {out }}=A B+B C_{i n}+A C_{i n} \\
& \bar{\Sigma}=\bar{A} \bar{B} C_{i n}+\bar{A} B \bar{C}_{i n}+A \bar{B} \bar{C}_{i n}+A B C_{i n}
\end{aligned}
$$

(4) Any SOP expression can be directly turned into circuit with inverters and $A N D-O R$
logic.


(5) When circuits are already in the AND-OR config., it is straight-forward to turn them into circuits which use only NAND gates.

First, we add the bubbles ...


Then, we convert Negative -OR to NAND and replace $\rightarrow$ wo- with $\rightarrow$ Don.


In fact, I con combine the two circuits in to one picture below:


Combinational Page 3

